

Direct measurement of the valley splitting in a few-electron silicon quantum dot using charge sensor source-drain bias spectroscopy

Kosuke Horibe¹, Tetsuo Kodera^{1,2,3} and Shunri Oda¹

¹ Quantum Nanoelectronics Research Center, Tokyo Institute of Technology

² Institute for Nano Quantum Information Electronics, the University of Tokyo

³ PRESTO, Japan Science and Technology Agency (JST)

One of the important topics for silicon based qubits is the investigation of the valley degeneracy [1], such as the valley splitting and physics of electron transition between the different valleys. In this work, we measured directly the valley splitting in a few-electron silicon QD using charge sensor source-drain bias spectroscopy at 300 mK.

We fabricated a double QD (DQD) and a charge sensor (CS) QD in a metal-oxide-semiconductor structure on a non-doped silicon-on-insulator substrate (Figure 1(a)). Few-electron occupancy states in the DQD are detected by measuring the transconductance dI_{CS}/dV_{SG2} of the CS (Figure 1(b)). Figure 1(d) shows the plot of dI_{CS}/dV_{SG2} as functions of the side gate voltage V_{SG2} and the source-drain bias voltage V_{DCS} on the CS. We observed two transition lines between the (0, 1) region and the (0, 2) region; the left and right lines in the Fig. 1(d) correspond to the lower and higher energy Γ valley on the conduction band in the right QD of the DQD in Fig. 1(a), respectively. When V_{DCS} is higher than 3.5 mV, the right side line appears because of the increase in the excitation rate Γ_3 of electrons from the lower energy valley to the higher energy valley (Figure 1(c)). This excitation is ascribed to the dissipated energy generated by the electrons which transport through the CS. We obtained the valley splitting $E_{VS} \approx 0.33$ meV.

This work was financially supported by KAKENHI (22246040, 24102703), JST-PRESTO, Yazaki Memorial Foundation for Science and Technology, and Project for Developing Innovation Systems of the Ministry of Education, Culture, Sports, Science and Technology (MEXT) and Global COE Program, "Photonics Integration-Core Electronics", MEXT, Japan.

[1] M. A. Eriksson *et al.*, Quantum Inf. Process. **3**, 133 (2004).

